

L Number	Hits	Search Text	DB	Time stamp
1	5080	((multiple adj1 functions) or (set near1 functions)) with (portion or part or region or section or segment or block or area)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 07:59
2	700	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:15
3	19928	326/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 08:05
4	26	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI) and 326/\$.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 08:05
5	17808	boolean	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:07
6	5	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI) and 326/\$.ccls.) and boolean	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 08:05
7	1169	((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:10
8	454	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; EPO; JPO; DERWENT	2003/10/30 10:16
9	4967	716/\$.ccls.	USPAT; EPO; JPO; DERWENT	2003/10/30 08:16
10	6	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))) and 716/\$.ccls.	USPAT; EPO; JPO; DERWENT	2003/10/30 08:16
11	0	identify\$3 near3 ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 09:31
12	124	identify\$3 near3 ((multiple adj1 functions) or (set near1 functions))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:20
13	37	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (identify\$3 near3 ((multiple adj1 functions) or (set near1 functions))))	USPAT; EPO; JPO; DERWENT	2003/10/30 10:07
14	6	Agrawal and (identify\$3 near3 ((multiple adj1 functions) or (set near1 functions))))	USPAT; EPO; JPO; DERWENT	2003/10/30 09:55

15	0	((portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and (Agrawal and (identify\$3 near3 ((multiple adj1 functions) or (set near1 functions))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 09:57
16	2	(circuit or (integrated adj1 circuit) or IC or VLSI or LSI) and (Agrawal and (identify\$3 near3 ((multiple adj1 functions) or (set near1 functions))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 09:57
17	14	"5696974"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:07
18	2	boolean and "5696974"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:08
19	0	(portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI) and "5696974"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:10
20	3	(circuit or (integrated adj1 circuit) or IC or VLSI or LSI) and "5696974"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:10
21	935	((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:15
22	246	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)))	USPAT; EPO; JPO; DERWENT	2003/10/30 10:17
23	19	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))) and 716/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2003/10/30 10:22
24	3	boolean and (((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))) and 716/\$.ccls.)	USPAT; EPO; JPO; DERWENT	2003/10/30 10:18
25	0	((identify\$3 near3 ((plurality adj1 functions) or (multiple adj1 functions) or (set near1 functions))) and (((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:22

26	21	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))) and 326/\$.ccls.	USPAT; EPO; JPO; DERWENT	2003/10/30 10:22
27	0	(identify\$3 near3 ((plurality adj1 functions) or (multiple adj1 functions) or (set near1 functions))) and (((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((plurality adj1 functions) or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))) and 326/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 10:22
-	135531	((integrated near1 circuit) or IC VLSI or LSI) and (design or simulat\$3 or layout)	USPAT; EPO; JPO; DERWENT	2003/10/29 15:13
-	129	((binary near2 decision near2 daigram) or BDD) and (((integrated near1 circuit) or IC VLSI or LSI) and (design or simulat\$3 or layout))	USPAT; EPO; JPO; DERWENT	2003/10/14 15:34
-	158	((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)	USPAT; EPO; JPO; DERWENT	2003/02/26 08:49
-	764	Boolean with (function and variable)	USPAT; EPO; JPO; DERWENT	2003/02/26 08:51
-	1292	Boolean same (function and variable)	USPAT; EPO; JPO; DERWENT	2003/02/26 08:52
-	555	(function near4 logic) and (Boolean same (function and variable))	USPAT; EPO; JPO; DERWENT	2003/02/26 08:53
-	45	((((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)) and (Boolean same (function and variable))	USPAT; EPO; JPO; DERWENT	2003/02/26 08:56
-	37	synthes\$6 and (((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)) and (Boolean same (function and variable))	USPAT; EPO; JPO; DERWENT	2003/02/27 11:18
-	17	(index or indices or indicator or pointer) and (((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)) and (Boolean same (function and variable))	USPAT; EPO; JPO; DERWENT	2003/02/26 09:09
-	1	(pair near4 (index or indices or indicator or pointer)) and (((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)) and (Boolean same (function and variable))	USPAT; EPO; JPO; DERWENT	2003/02/26 09:01
-	1	((pivot\$2 or crucial or cardinal or central or fundamental or essential) near4 function) and ((index or indices or indicator or pointer) and (((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)) and (Boolean same (function and variable)))	USPAT; EPO; JPO; DERWENT	2003/02/27 11:20

-	5	((pivot\$2 or crucial or cardinal or central or fundamental or essential) with function) and ((index or indices or indicator or pointer) and (((binary near2 decision near2 daigram) or BDD) and ((integrated near1 circuit) or IC or VLSI or LSI)) and (Boolean same (function and variable))))	USPAT; EPO; JPO; DERWENT	2003/02/26 13:28
-	4	"5937183"	USPAT; EPO; JPO; DERWENT	2003/02/27 09:24
-	2	(data with stor\$3) and "5937183"	USPAT; EPO; JPO; DERWENT	2003/02/26 14:10
-	14	((("6295517") or ("6230303") or ("6112023") or ("5937183") or ("5796623") or ("5682321") or ("5625567")).PN.	USPAT; EPO; JPO; DERWENT	2003/02/26 14:04
-	2	(data with stor\$3) and ((data with stor\$3) and "5937183")	USPAT; EPO; JPO; DERWENT	2003/02/26 14:05
-	5	"6301687"	USPAT; EPO; JPO; DERWENT	2003/02/27 09:44
-	18	((("5937183") or ("5787006") or ("5754454") or ("5671399") or ("5649165") or ("5638381") or ("5469367") or ("5331568") or ("5243538")).PN.	USPAT; EPO; JPO; DERWENT	2003/02/27 09:51
-	10	boolean and (((("5937183") or ("5787006") or ("5754454") or ("5671399") or ("5649165") or ("5638381") or ("5469367") or ("5331568") or ("5243538")).PN.)	USPAT; EPO; JPO; DERWENT	2003/02/27 09:55
-	5	(replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (boolean and (((("5937183") or ("5787006") or ("5754454") or ("5671399") or ("5649165") or ("5638381") or ("5469367") or ("5331568") or ("5243538")).PN.))	USPAT; EPO; JPO; DERWENT	2003/10/29 15:40
-	2	((pivot\$2 or crucial or cardinal or central or fundamental or essential) with function) and ((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (boolean and (((("5937183") or ("5787006") or ("5754454") or ("5671399") or ("5649165") or ("5638381") or ("5469367") or ("5331568") or ("5243538")).PN.))	USPAT; EPO; JPO; DERWENT	2003/02/27 11:20
-	19) "5649165"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 15:32
-	13	((binary near2 decision near2 daigram) or BDD) and "5649165"	USPAT; EPO; JPO; DERWENT	2003/10/14 15:34
-	12	(replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and (((binary near2 decision near2 daigram) or BDD) and "5649165")	USPAT; EPO; JPO; DERWENT	2003/10/30 08:15
-	12	function and ((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((binary near2 decision near2 daigram) or BDD) and "5649165"))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:12
-	5	(set near2 function) and ((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((binary near2 decision near2 daigram) or BDD) and "5649165"))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:28

-	239088	((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:27
-	748	((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 08:53
-	61	(((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; EPO; JPO; DERWENT	2003/10/15 08:52
-	35	(replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:35
-	11	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; EPO; JPO; DERWENT	2003/10/14 16:40
-	11	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:42
-	889	((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:41

-	85	(((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:41
-	18	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and ((((\$4tabulat\$3 or (lookup adj1 table) or library) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((set near2 function) with (sub-circuit or (sub adj US-5649165-A.DID. circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/14 16:42
-	0	((set near2 function) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:11
-	6	((set near2 function) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:10
-	0	((set near2 function) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 08:58
-	0	((set near2 functions) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design\$3 or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 08:58
-	2	((set near2 functions) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network))	USPAT; EPO; JPO; DERWENT	2003/10/15 08:59
-	12	((set near2 functions) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:21
-	152907	((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4)	USPAT; EPO; JPO; DERWENT	2003/10/15 09:03
-	186448	((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:03
-	0	((set near2 function) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:04

-	6	((set near2 function) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; EPO; JPO; DERWENT	2003/10/15 09:08
-	6455	(set near2 functions) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:10
-	179	(sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network) and ((set near2 functions) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:11
-	6	((set near2 functions) with (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:12
-	13	((set near2 functions) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network)) and (((integrated near1 circuit) or IC or VLSI or LSI) and (design or simulat\$3 or model\$4))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:12
-	21	((set near2 functions) same (sub-circuit or (sub adj1 circuit) or (sub adj1 network) or sub-network))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/15 09:21
-	37435	(functions or (multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:11
-	475	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:27
-	26	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (portion or part or region or section or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and (((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:14
-	700	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 08:04
-	96	optimiz\$5 and (((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:13

-	5	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (portion or part or region or section or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and (optimiz\$5 and ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:08
-	43	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (portion or part or region or section or area)) and (optimiz\$5 and ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:25
-	7	boolean and ((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (portion or part or region or section or area)) and (optimiz\$5 and ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:26
-	663	((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:58
-	2	((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 16:52
-	1168	((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/30 08:14
-	3	((library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:02
-	6190	716/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:07
-	27	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and 716/\$.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:08

-	7	((replac\$5 or displac\$5 or supersed\$5 or succeed\$5 or supplant\$5 reinstat\$5) with (portion or part or region or section or area)) and (((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and 716/\$.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:26
-	89	((("0019729") or ("6171731") or ("6130750") or ("6081659") or ("6081658") or ("6078738") or ("6076465") or ("6016357") or ("6011911") or ("6009251") or ("6009250") or ("5863682") or ("5862058") or ("5849440") or ("5847959") or ("5827623") or ("5825647") or ("5815685") or ("5804340") or ("5801954") or ("5795688") or ("5740068") or ("5707765") or ("5705301") or ("5702848") or ("5663893") or ("5657235") or ("5631110") or ("5572598") or ("5553273") or ("5538815") or ("5533148") or ("5532090") or ("5447810") or ("5432714") or ("5424154") or ("5340700") or ("5326659") or ("5316878") or ("5256505") or ("5242770") or ("5241185") or ("5182718") or ("5051598") or ("4812962")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:26
-	0	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and (((("0019729") or ("6171731") or ("6130750") or ("6081659") or ("6081658") or ("6078738") or ("6076465") or ("6016357") or ("6011911") or ("6009251") or ("6009250") or ("5863682") or ("5862058") or ("5849440") or ("5847959") or ("5827623") or ("5825647") or ("5815685") or ("5804340") or ("5801954") or ("5795688") or ("5740068") or ("5707765") or ("5705301") or ("5702848") or ("5663893") or ("5657235") or ("5631110") or ("5572598") or ("5553273") or ("5538815") or ("5533148") or ("5532090") or ("5447810") or ("5432714") or ("5424154") or ("5340700") or ("5326659") or ("5316878") or ("5256505") or ("5242770") or ("5241185") or ("5182718") or ("5051598") or ("4812962")).PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:27
-	6531	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or area)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:42

-	0	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or area)) and (((("0019729") or ("6171731") or ("6130750") or ("6081659") or ("6081658") or ("6078738") or ("6076465") or ("6016357") or ("6011911") or ("6009251") or ("6009250") or ("5863682") or ("5862058") or ("5849440") or ("5847959") or ("5827623") or ("5825647") or ("5815685") or ("5804340") or ("5801954") or ("5795688") or ("5740068") or ("5707765") or ("5705301") or ("5702848") or ("5663893") or ("5657235") or ("5631110") or ("5572598") or ("5553273") or ("5538815") or ("5533148") or ("5532090") or ("5447810") or ("5432714") or ("5424154") or ("5340700") or ("5326659") or ("5316878") or ("5256505") or ("5242770") or ("5241185") or ("5182718") or ("5051598") or ("4812962"))).PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:28
-	13	((Library or (look adj1 up adj1 table)) with (portion or part or region or section or segment or block or area) same (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and ((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or area))	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:34
-	2	"6578188"	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:40
-	3	("6453452").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:40
-	0	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and (("6453452").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:42
-	0	((multiple adj1 functions) or (set near2 functions)) with (portion or part or region or section or segment or block or area) with (circuit or (integrated adj1 circuit) or IC or VLSI or LSI)) and (("6453452").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT	2003/10/29 17:43